

# PTP @ Meta

Oleg Obleukhov  
Production Engineer



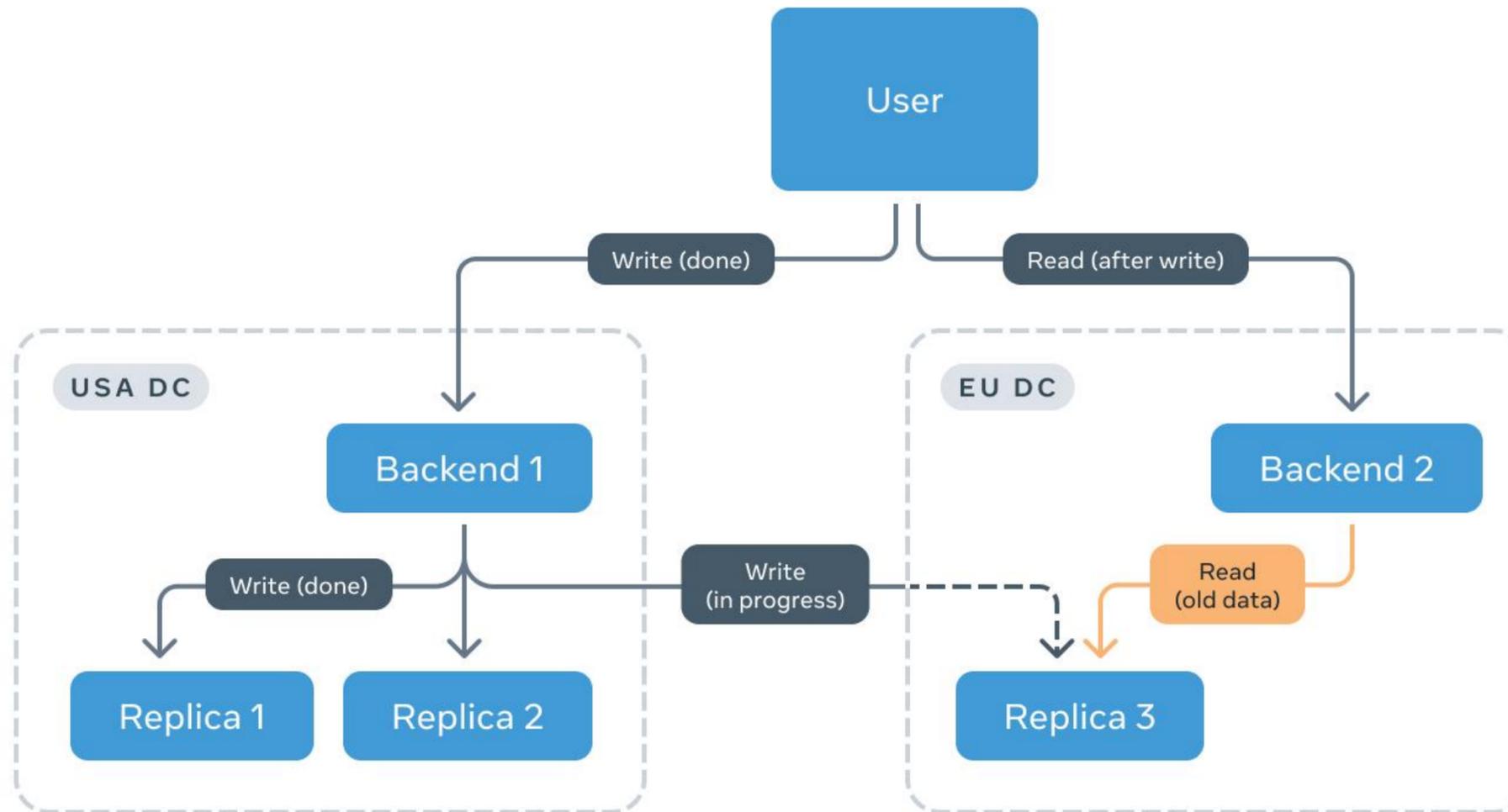
# Agenda

- The case for PTP
- The PTP Architecture
  - The PTP rack
  - The PTP network
  - The PTP client
- How we monitor the PTP architecture

# The case for PTP

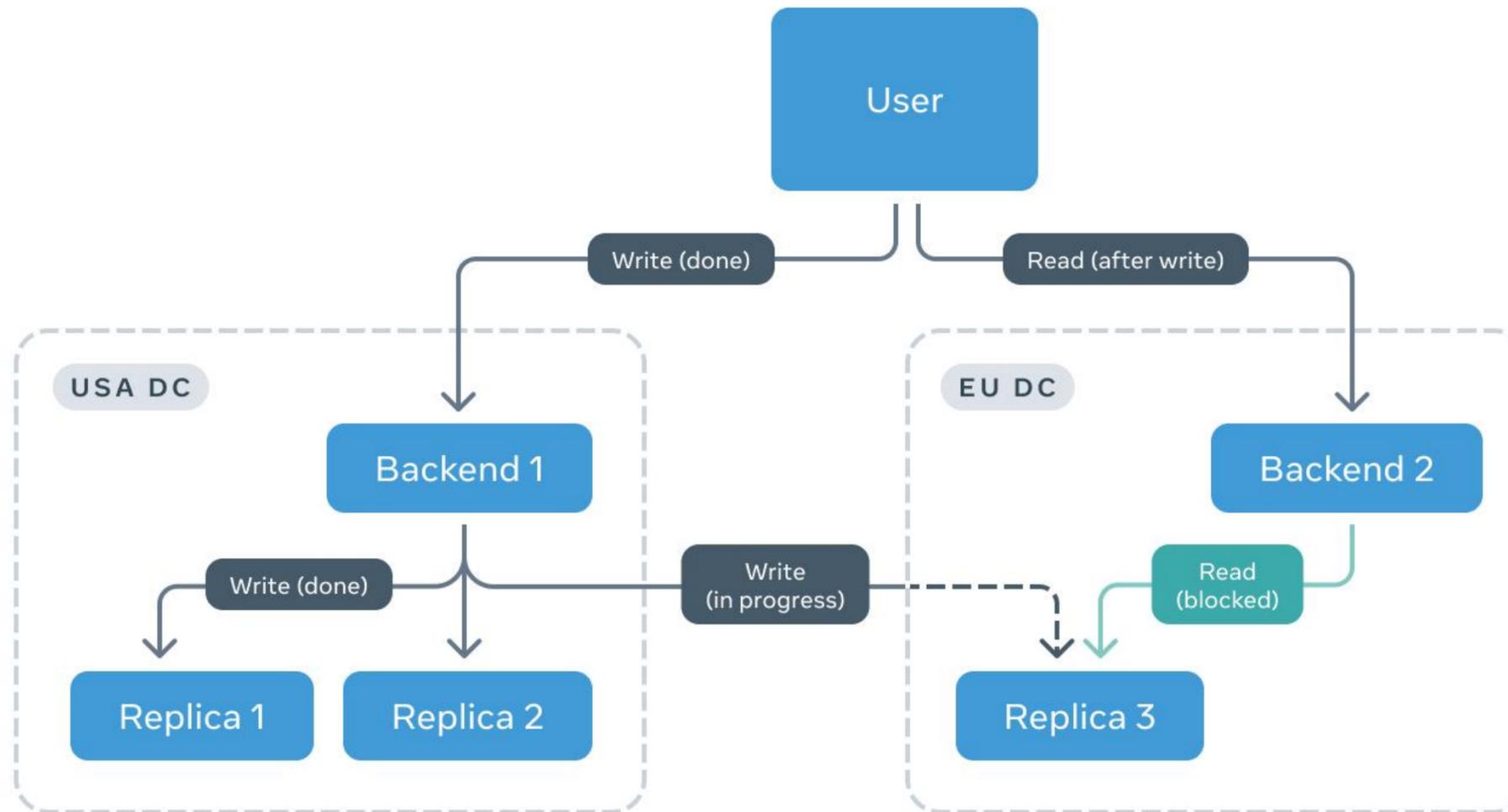
## Schematic representation of read returning outdated information

### Commit-wait ensuring consistency guarantee (linearizability)



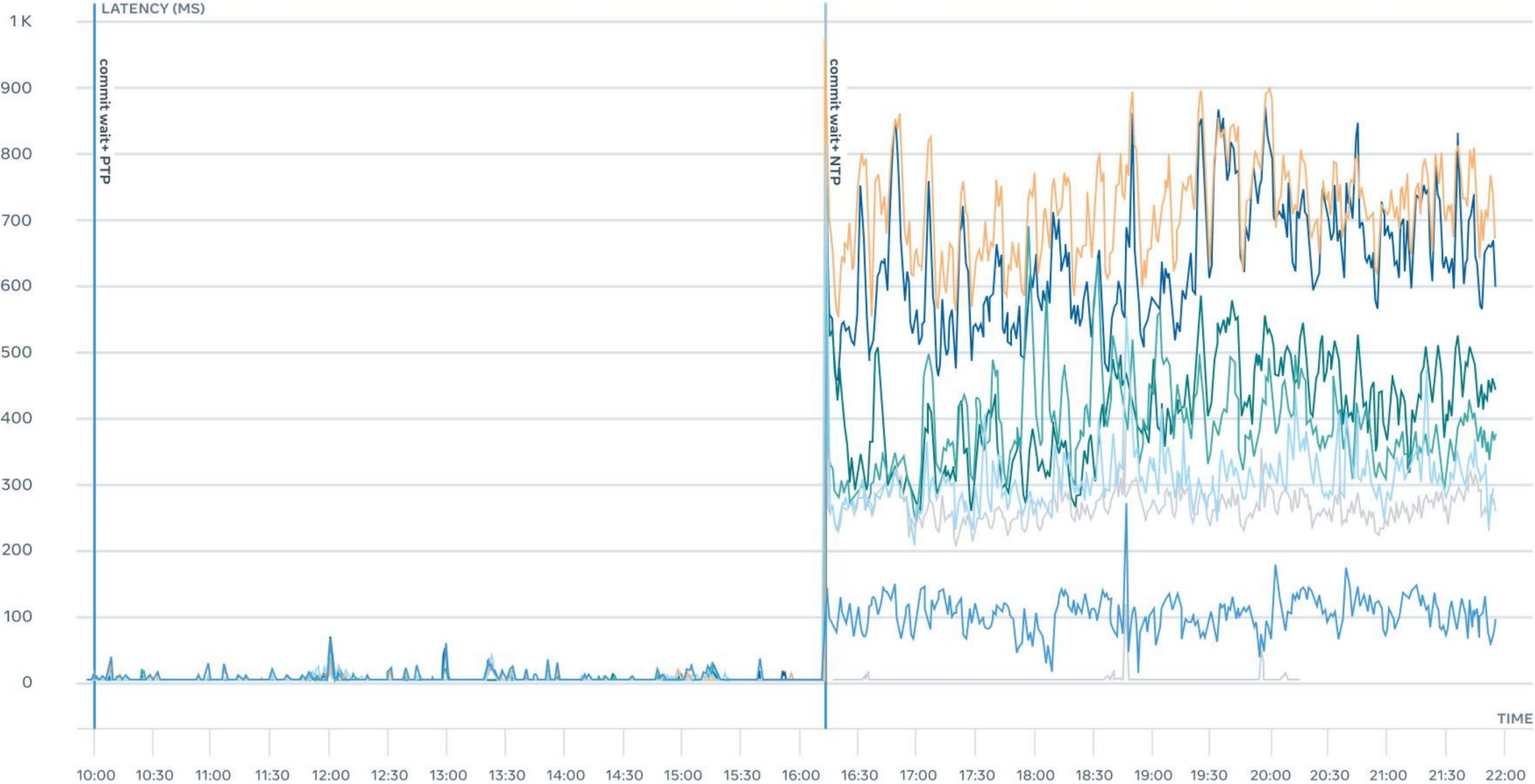
## Schematic representation of read returning outdated information

### Commit-wait ensuring consistency guarantee (linearizability)



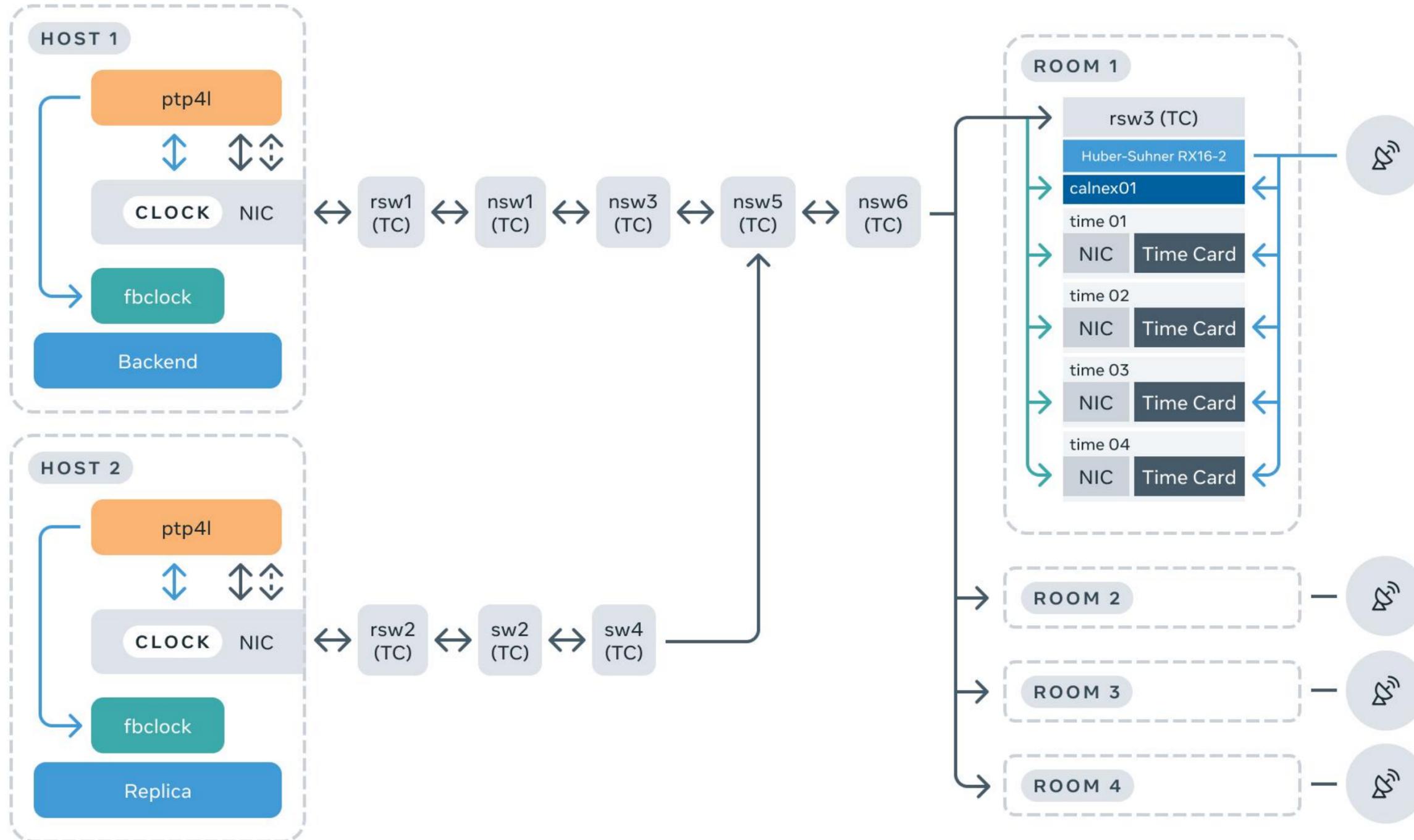
Commit-wait reads issued against PTP and NTP backed clusters

# Commit-wait reads issued against PTP and NTP backed clusters



# The PTP architecture

# Regional PTP architecture



# The PTP rack

# GNSS



GNSS antenna in one of the Meta Data Center location



Huber-Suhner GNSS-over-fiber technology tested in Meta Dublin office

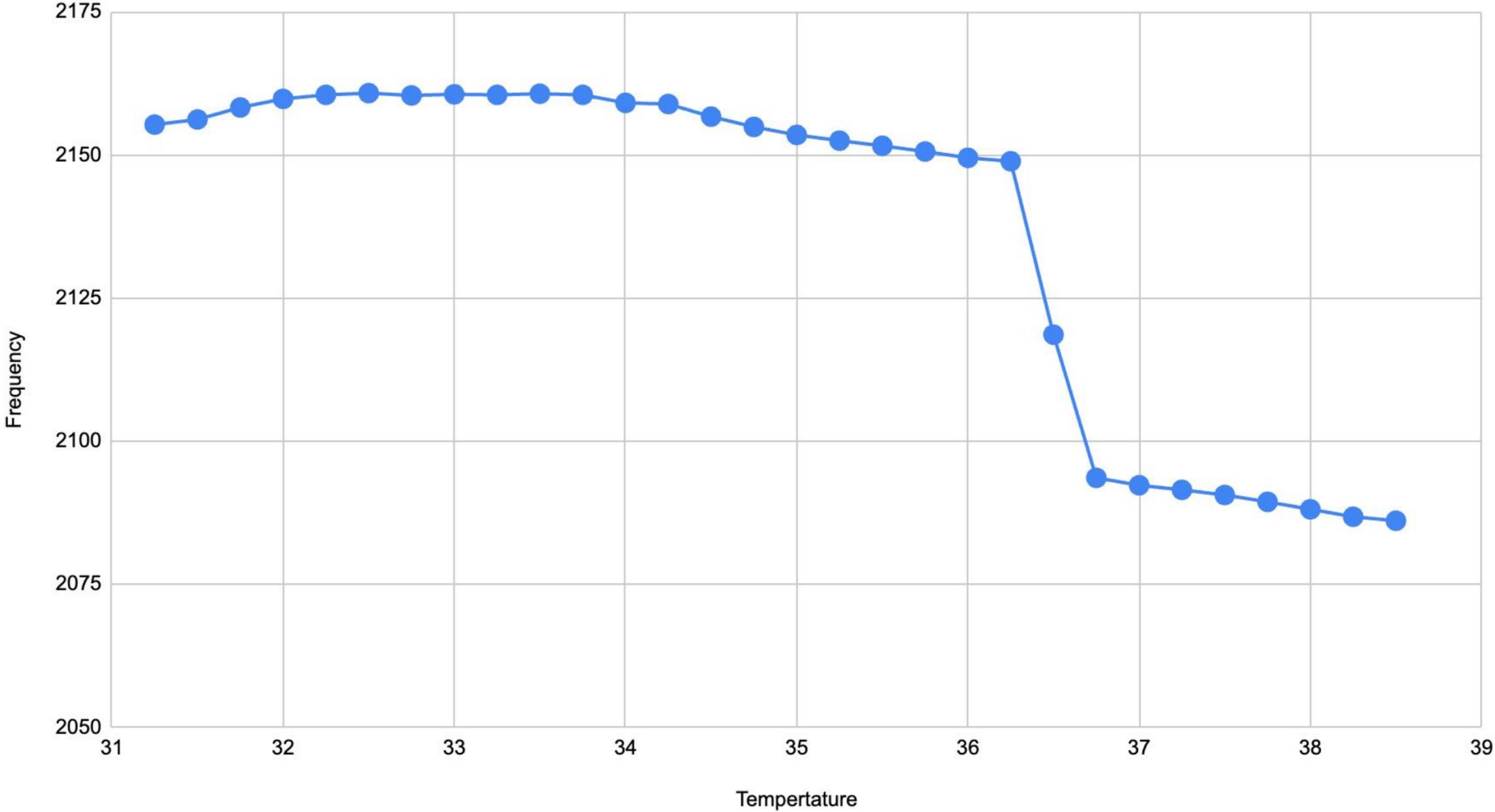
## The Time Card

### Facebook Time Card



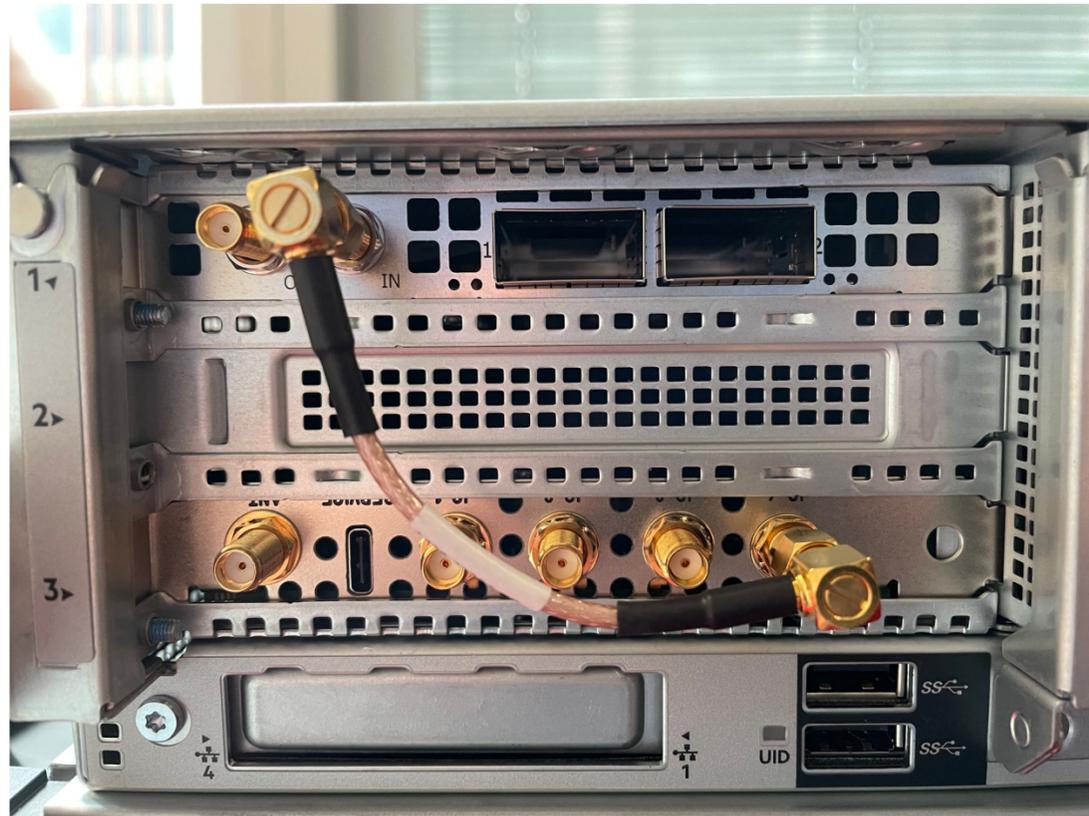
# The Time Card

## Frequency vs. Temperature



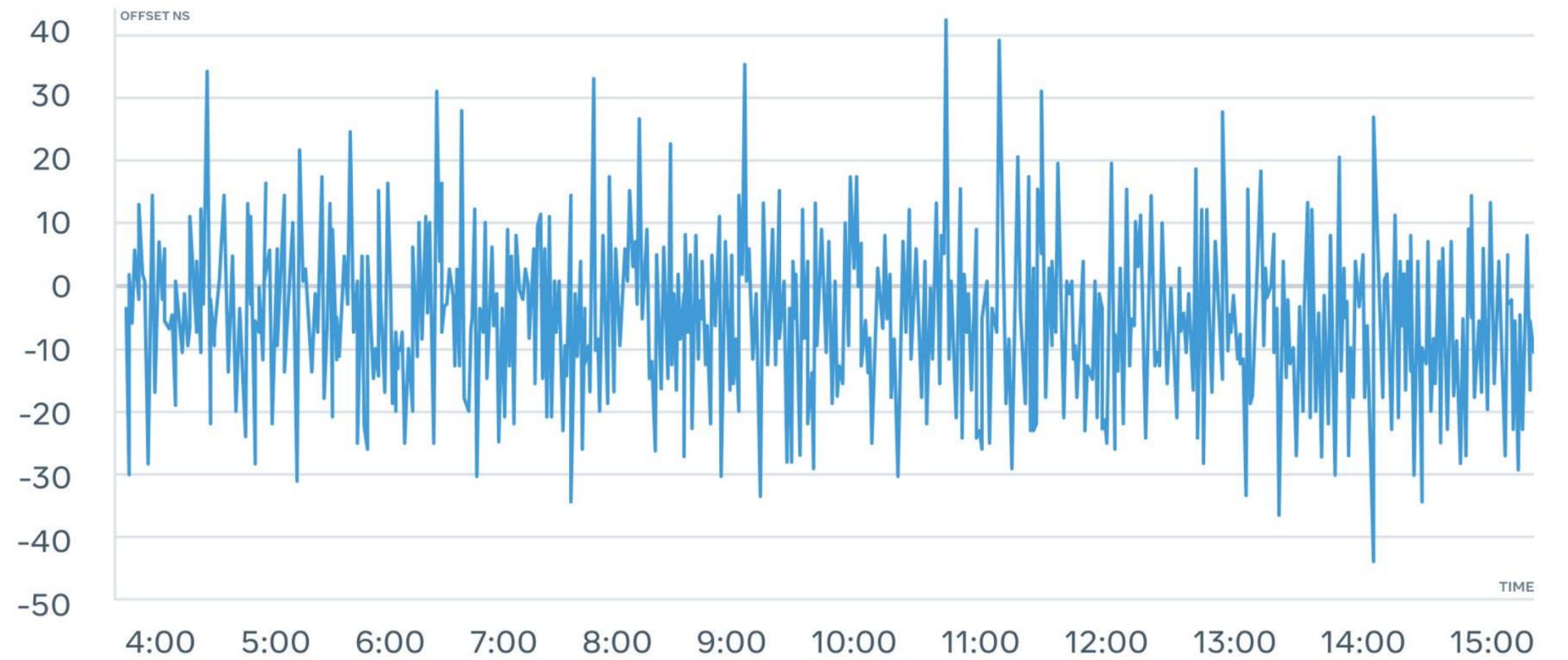
Frequency to temperature ratio of an atomic clock

## The Time Card



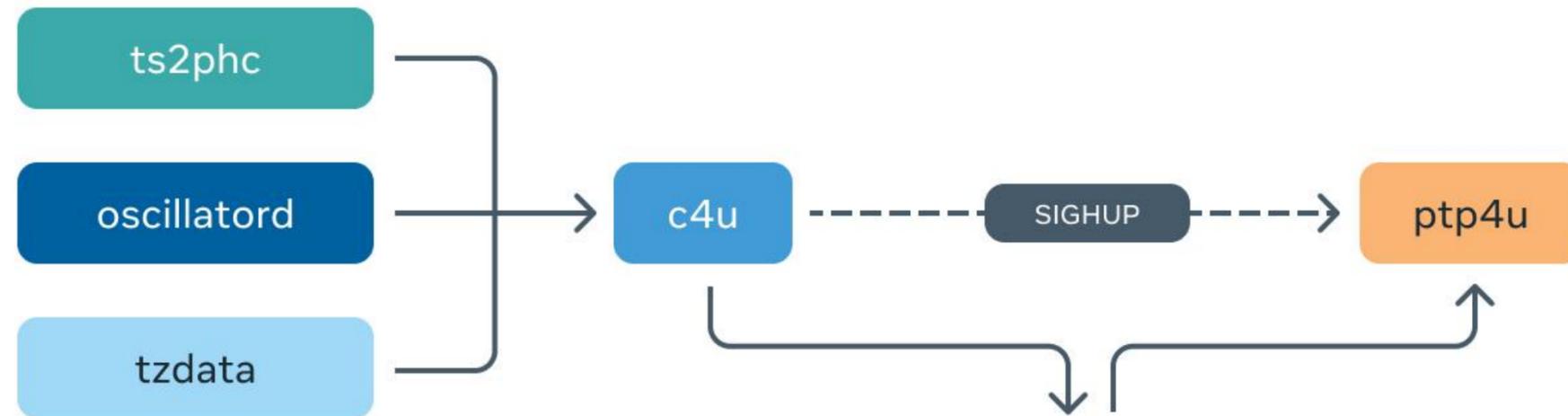
Short cable between PPS-out of the Time Card and PPS-in of the NIC

## Offset between the Time Card and the Network Card PHC





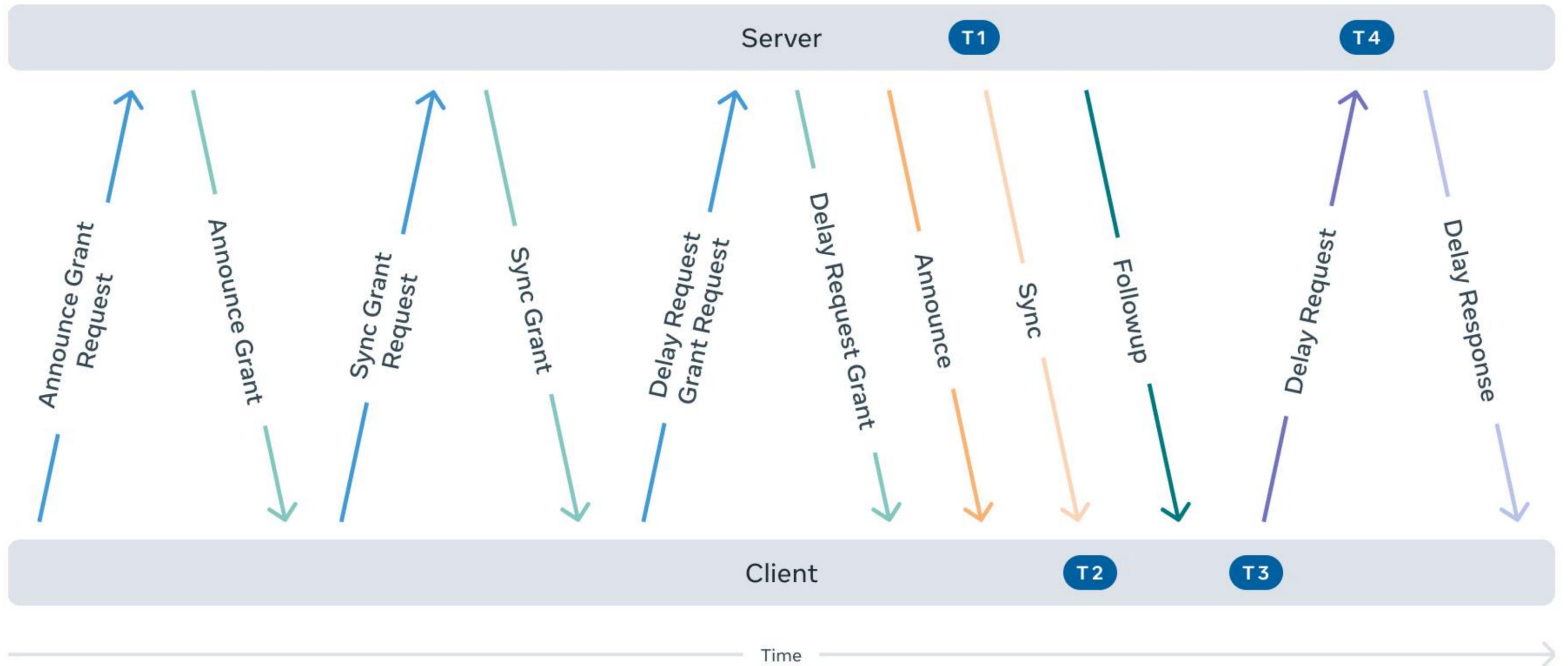
## C4U architecture



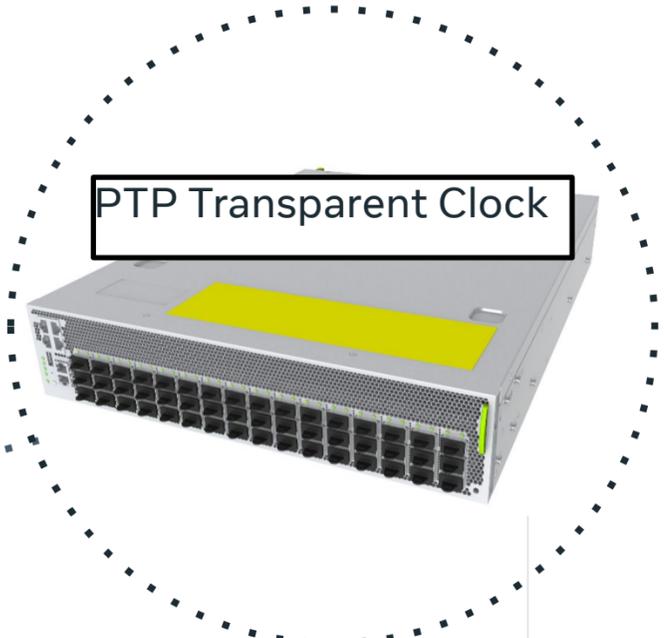
```
$ cat /etc/ptp4u.yaml  
clockaccuracy: 34  
clockclass: 6  
draininterval: 30s  
maxsubduration: 1h0m0s  
metricinterval: 1m0s  
minsubinterval: 1s  
utcoffset: 37s
```

# The PTP network

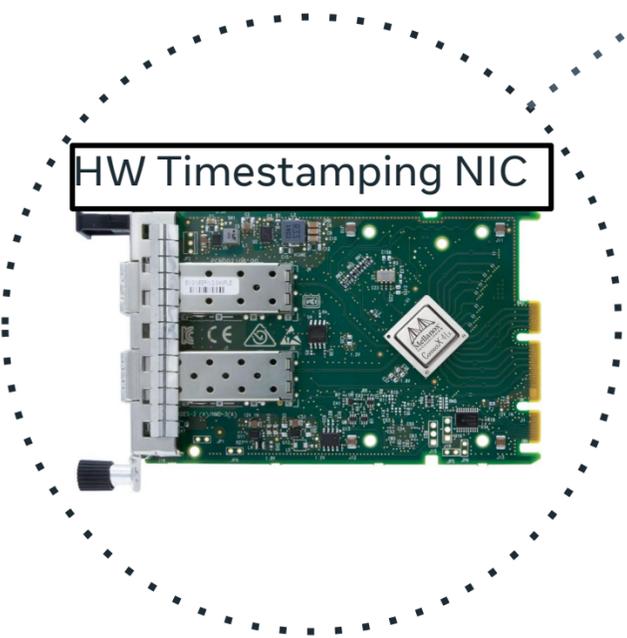
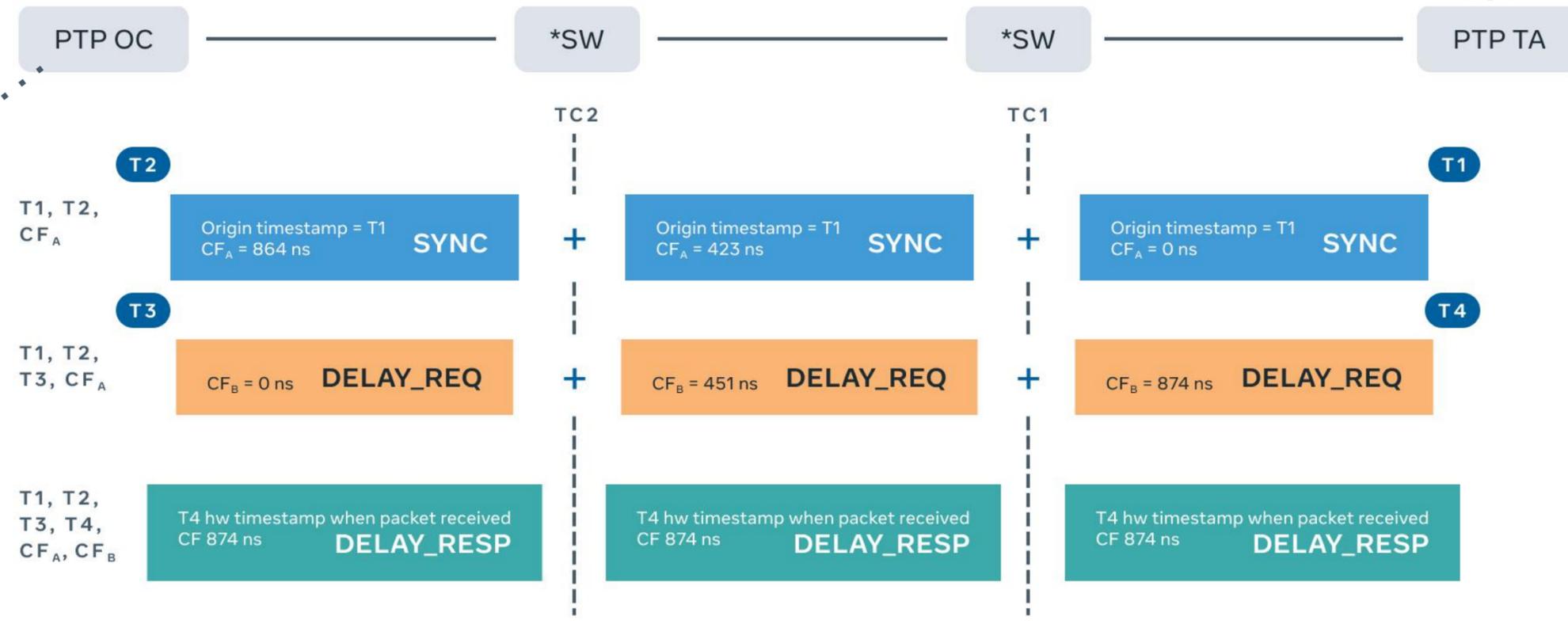
# Two-step PTP exchange



# Transparent clock



## Transparent Clock and Correction Field



PTP HW TIMESTAMPING

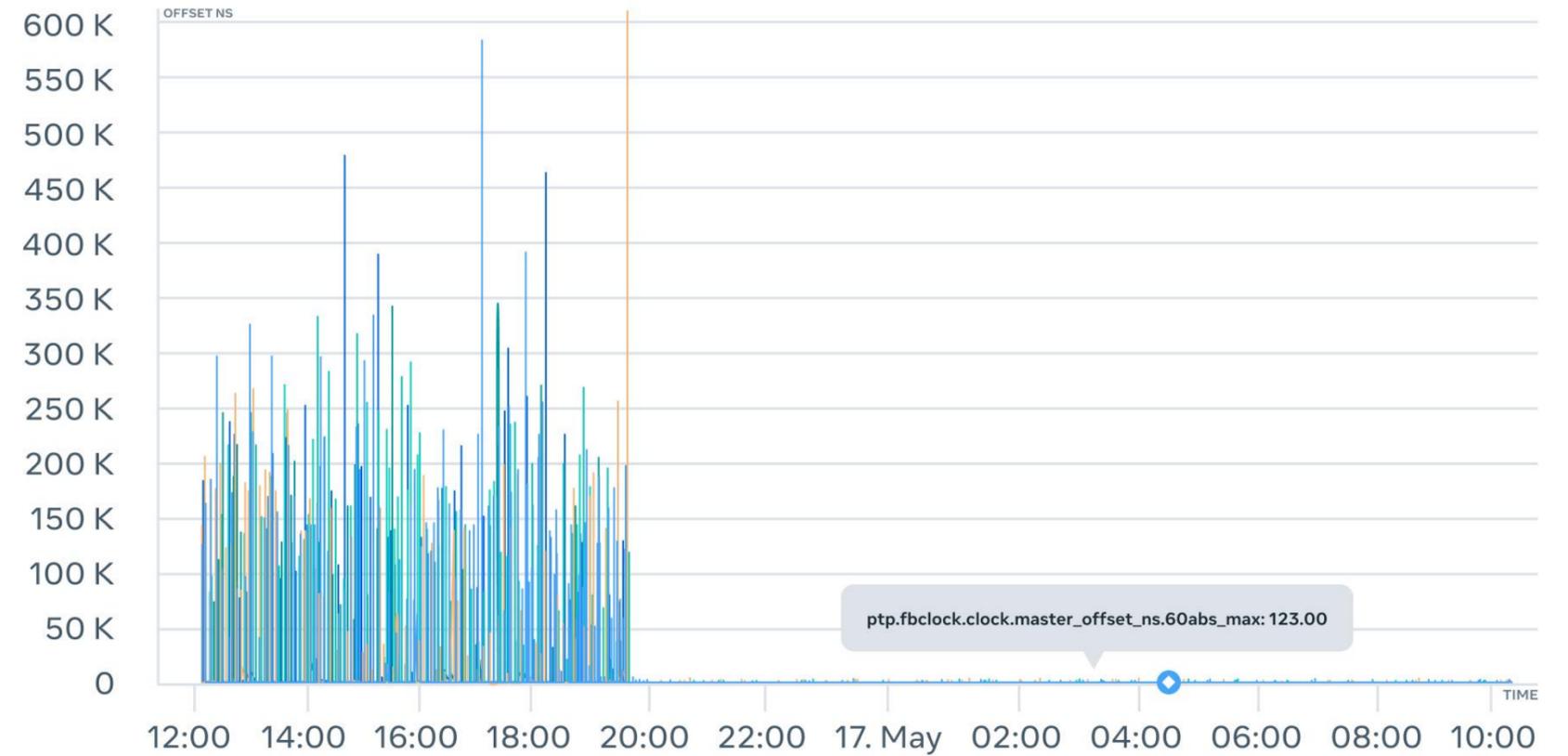
$$\text{mean\_path\_delay} = ((T4 - T3) + (T2 - T1) - CF_A - CF_B) / 2$$

$$\text{clock\_offset} = (T2 - T1) - \text{mean\_path\_delay}$$

## Transparent clock impact

ptp41[43.662]: offset	-9	s2	freq	-12372	path	delay	4114
ptp41[44.662]: offset	17	s2	freq	-12349	path	delay	4114
ptp41[45.662]: offset	37	s2	freq	-12324	path	delay	4078
ptp41[46.662]: offset	-70	s2	freq	-12420	path	delay	4153
ptp41[47.662]: offset	95	s2	freq	-12276	path	delay	4039
ptp41[48.662]: offset	266776	s2	freq	+254434	path	delay	4181
ptp41[49.662]: offset	-430864	s2	freq	-363173	path	delay	168255
ptp41[50.662]: offset	-80141	s2	freq	-141710	path	delay	168255
ptp41[51.662]: offset	217086	s2	freq	+131475	path	delay	408
ptp41[52.662]: offset	16268	s2	freq	-4217	path	delay	57459
ptp41[53.662]: offset	8101	s2	freq	-7504	path	delay	57459
ptp41[54.662]: offset	55912	s2	freq	+42738	path	delay	4776
ptp41[56.305]: offset	-48984	s2	freq	-45385	path	delay	19209
ptp41[56.662]: offset	-37194	s2	freq	-48290	path	delay	19209
ptp41[57.662]: offset	29964	s2	freq	+7710	path	delay	-12022
ptp41[58.662]: offset	9943	s2	freq	-3322	path	delay	-12022
ptp41[59.662]: offset	-19403	s2	freq	-29685	path	delay	8279
ptp41[60.662]: offset	8560	s2	freq	-7543	path	delay	-2377
ptp41[61.662]: offset	-4906	s2	freq	-18441	path	delay	6256
ptp41[62.662]: offset	4197	s2	freq	-10810	path	delay	3249
ptp41[63.662]: offset	979	s2	freq	-12769	path	delay	4917
ptp41[64.662]: offset	1386	s2	freq	-12068	path	delay	4917
ptp41[65.662]: offset	1741	s2	freq	-11297	path	delay	4270
ptp41[66.662]: offset	509	s2	freq	-12007	path	delay	4428
ptp41[67.662]: offset	395	s2	freq	-11968	path	delay	4185
ptp41[68.662]: offset	-7	s2	freq	-12252	path	delay	4185

## Absolute offset values on hosts connected to the switch without Transparent Clock enabled



# The PTP client

## Hardware timestamps

```
$ ethtool -T eth0
```

```
Time stamping parameters for eth0:
```

```
Capabilities:
```

```
hardware-transmit
```

```
hardware-receive
```

```
hardware-raw-clock
```

```
PTP Hardware Clock: 0
```

```
Hardware Transmit Timestamp Modes:
```

```
off
```

```
on
```

```
Hardware Receive Filter Modes:
```

```
none
```

```
All
```

```
ptp41[40.432]: offset      -16 s2 freq  -13105 path delay      3493
```

```
ptp41[41.432]: offset      -6 s2 freq  -13100 path delay      3493
```

```
ptp41[42.432]: offset       9 s2 freq  -13087 path delay      3493
```

```
ptp41[43.432]: offset      -5 s2 freq  -13098 path delay      3493
```

```
ptp41[44.432]: offset       1 s2 freq  -13093 path delay      3493
```

```
ptp41[45.432]: spike detected => max_offset_locked: 33, setting offset to min_offset_freq_mean: -13065.039314
```

```
ptp41[46.432]: skip 1/15 large offset (>33) 224401
```

```
ptp41[47.432]: offset     -21 s2 freq  -13115 path delay      3493
```

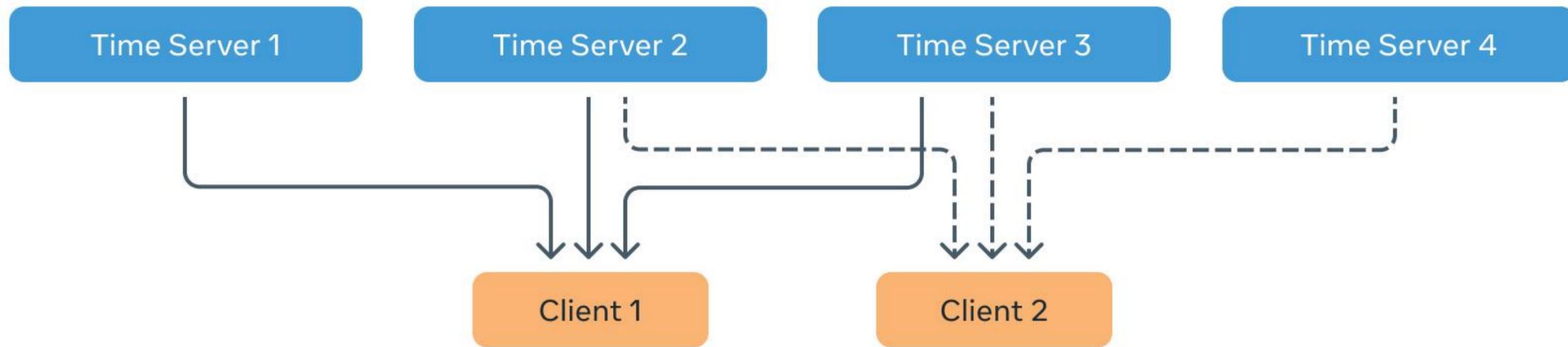
```
ptp41[48.432]: offset       9 s2 freq  -13091 path delay      3493
```

```
ptp41[49.432]: offset      10 s2 freq  -13088 path delay      3493
```

```
ptp41[50.432]: offset      -8 s2 freq  -13103 path delay      3493
```

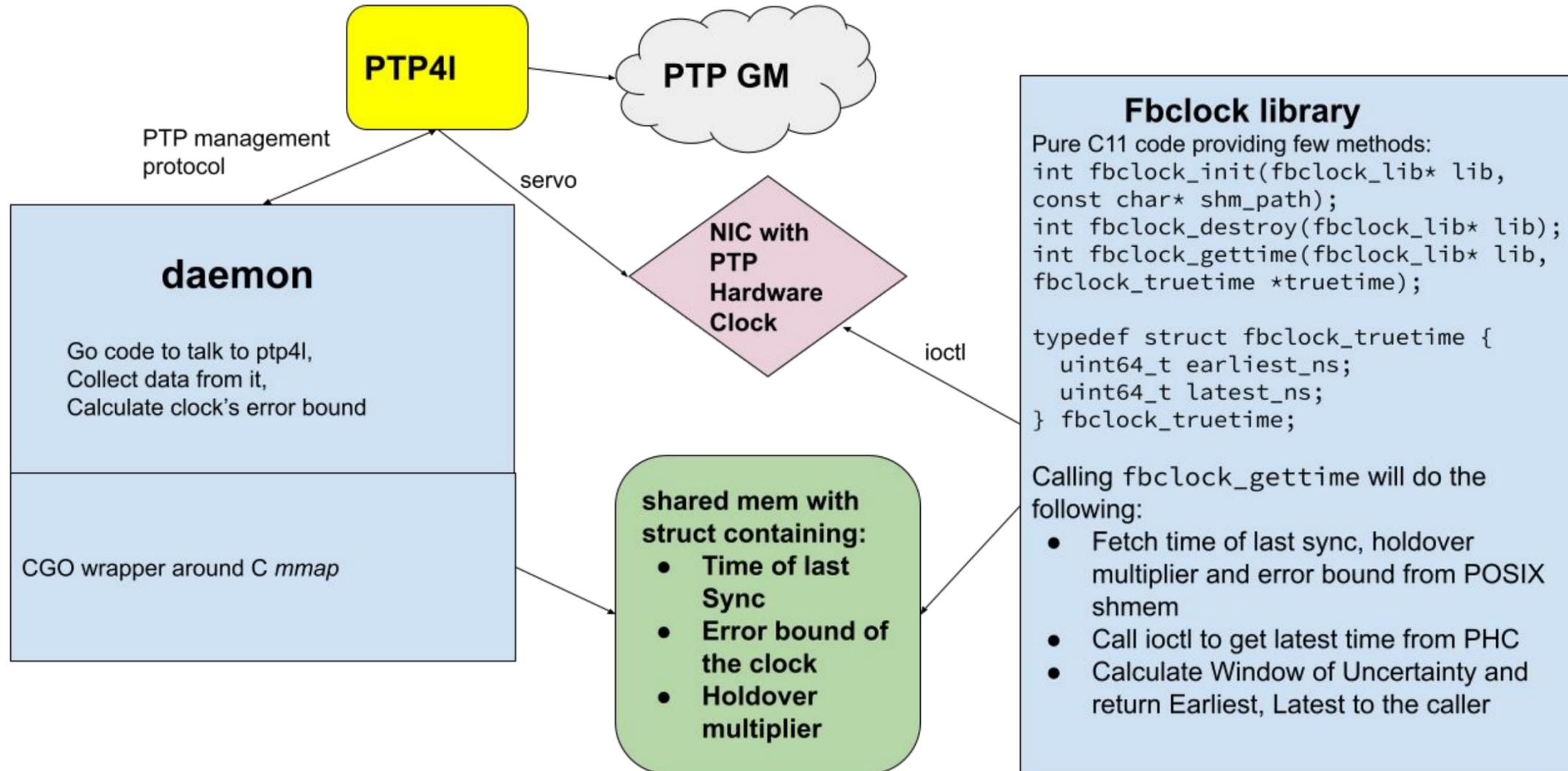
128 bits	64 bits	64 bits	64 bits
Socket control message header	Software Timestamp	Legacy Timestamp	Hardware Timestamp

# Sharding



Schematic representation of sharding

# fbclock

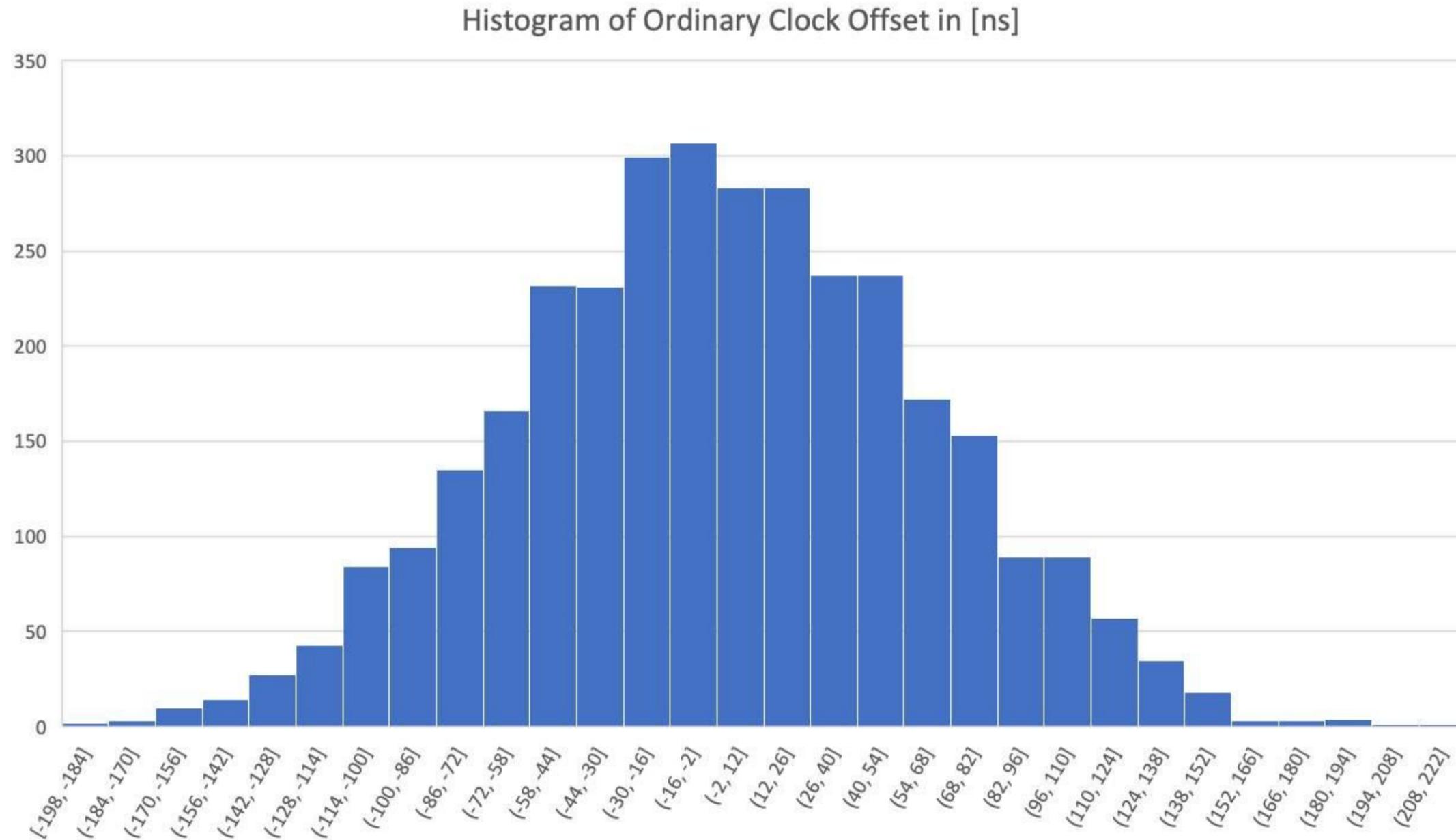


## fbclock

Estimated E2E Variance = [GNSS Variance + MAC Variance + ts2phc Variance] + [PTP4L Offset Variance] = [Time Server Variance] + [Ordinary Clock Variance]

Estimated E2E Variance =  $(12\text{ns}^2) + (43\text{ns}^2) + (52\text{ns}^2) + (61\text{ns}^2) = 8418$  which corresponds to 91.7 ns

$$\text{Var}\left(\sum_{i=1}^n X_i\right) = \sum_{i=1}^n \text{Var}(X_i)$$

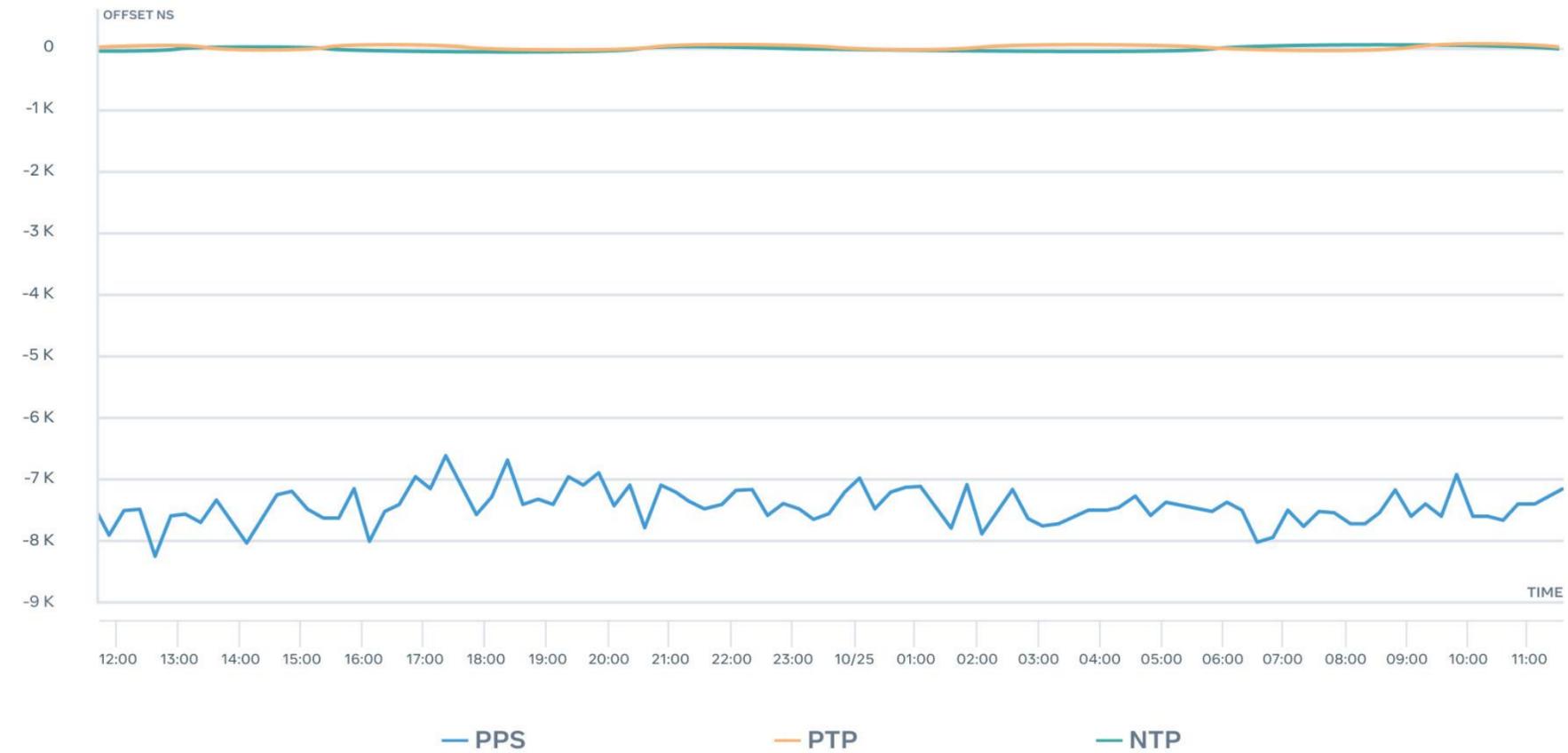


# How we monitor the PTP architecture

Calnex



### Calnex Sentinel monitoring data



Calnex Sentinel monitoring data

## ptpcheck

```
$ ptpcheck diag
```

```
[ OK ] GM is present
```

```
[ OK ] Period since last ingress is 972.752664ms, we expect it to be within 1s
```

```
[ OK ] GM offset is 67ns, we expect it to be within 250µs
```

```
[ OK ] GM mean path delay is 3.495µs, we expect it to be within 100ms
```

```
[ OK ] Sync timeout count is 1, we expect it to be within 100
```

```
[ OK ] Announce timeout count is 0, we expect it to be within 100
```

```
[ OK ] Sync mismatch count is 0, we expect it to be within 100
```

```
$ ptpcheck fbclock
```

```
{"earliest_ns":1654191885711023134,"latest_ns":1654191885711023828,"wou_ns":694}
```

```
$ ptpcheck phcdiff -d /dev/ptp0 -d /dev/ptp2
```

```
PHC offset: -15ns
```

```
Delay for PHC1: 358ns
```

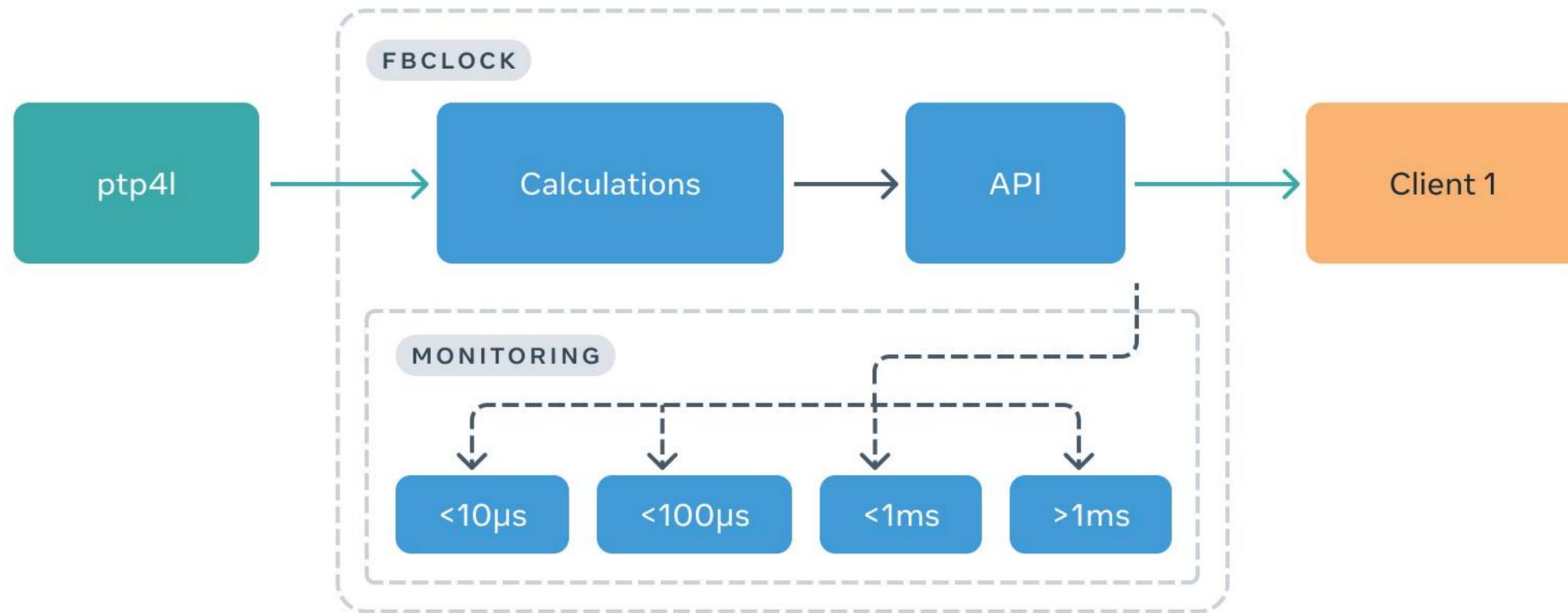
```
Delay for PHC2: 2.588µs
```

```
$ ptpcheck sources
```

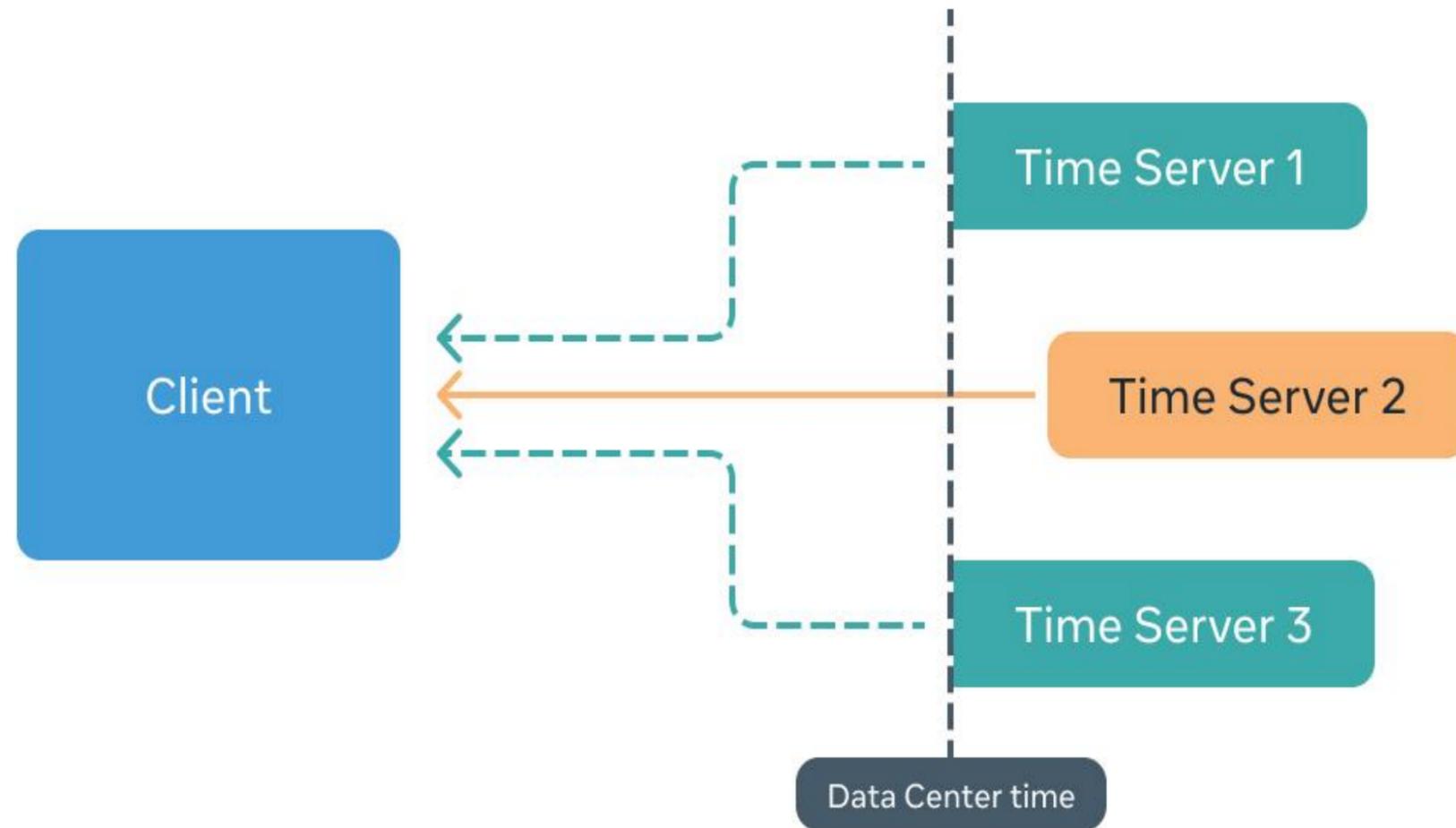
SELECTED	IDENTITY	ADDRESS	STATE	CLOCK	VARIANCE	P1:P2	OFFSET (NS)	DELAY (NS)	LAST SYNC
true	abcdef.ffffe.111111-1	time01.example.com.	HAVE_SYDY	6:0x22	0x59e0	128:128	27	3341	868.729197ms
false	abcdef.ffffe.222222-1	time02.example.com.	HAVE_ANN	6:0x22	0x59e0	128:128			
false	abcdef.ffffe.333333-1	time03.example.com.	HAVE_ANN	6:0x22	0x59e0	128:128			

## Fbclock API monitoring

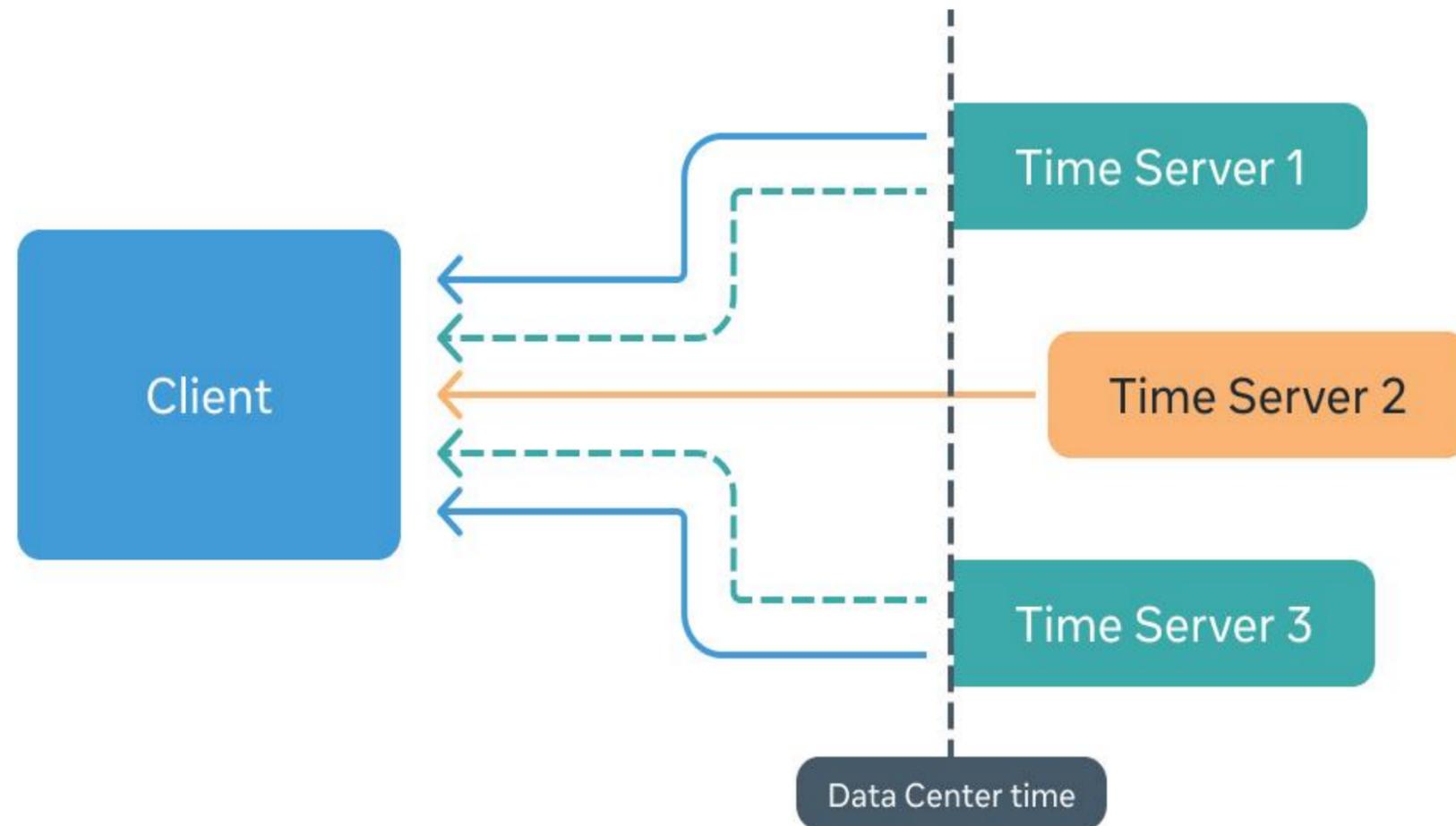
$\mu$ s microsecond      ms millisecond



# Client following Time Server 2



# Client following Time Server 2



**Thank you**

